PTO/SB/21 (09-04)

Approved for use through 07/31/2006. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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SIL S.			Application Numb	er	09/832,73	38
RANSM			Filing Date		April 10, 2	2001
N 1 0 2005 5 FOR	IVI		First Named Inver	ntor	Robert W	. Corrigan
J.			Art Unit		2611	
used for all correspond	dence after initial	filina)	Examiner Name	··················	Nalevank	o, Christopher R
Total Number of Pages in This		11	Attorney Docket N	lumber	SLM-0000	00 (P0076)
		ENCLO	SURES (check all th	at apply)	•	
Fee Transmittal Form		Drawing(s			After Al	lowance Communication to TC
Fee Attached		Licensing	-related Papers			Communication to Board eals and Interferences
Amendment / Reply		Petition				Communication to TC Notice, Brief, Reply Brief)
After Final			Convert to a al Application		☐ Propriet	tary Information
Affidavits/declaration	(s)		Attorney, Revocation f Correspondence Add	dress	Status L	Letter
Extension of Time Reque	st	Terminal [Disclaimer		Other E	Enclosure(s) dentify below):
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Information Disclosure St	atement	☐ Lan	dscape Table on CD			vers of Attorney; eipt Postcard
Certified Copy of Priority Document(s)	Re	emarks				
Reply to Missing Parts/ Incomplete Application		÷				
Reply to Missing Par under 37 CFR1.52 or						
	SIGNATU	JRE OF A	APPLICANT, ATTO	RNEY, OF	R AGENT	·
Firm	окл	AMOTO & BI	ENEDICTO LLP			
Signature	Pa	ti'a Be	ud w			
Printed Name	Patr	ick D. Bened	dicto			
Date	Janı	uary 5, 2005		Reg. No.	40,909	
	CE	RTIFICAT	TE OF TRANSMISS	ION/MAII	LING	
	ge as first cla	ass mail ir				with the United States Postal for Patents, P.O. Box 1450,
Signature	pania 6	ean	· ·			
Typed or printed name	Patrick D. Ben	edicto			Date	January 5, 2005

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

PTO/SB/122 (09-04)
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CHANGE OF CORRESPONDENCE ADDRESS Application

Address to: **Commissioner for Patents** P.O. Box 1450 Alexandria, VA 22313-1450.

Application Number	09/832,738
Filling Date	April 10, 2001
First Named Inventor	Robert W. Corrigan
Art Unit	2611
Examiner Name	Nalevanko, Christopher R
Attorney Docket Number	SLM-00000 (P0076)

Please change the Correspondence Address for the a	above-identified applic	ation to:
The address associated with Customer Number:	31894	7
OR		_
Firm <i>or</i> Individual Name	4	
Address		
City	State	Zip
Country		
Telephone	Fax	
declaration. See 37 CFR 1.33(a)(1). Re	Request for Customer Nul osed (Form PTO/SB/96). In Number 40,909.	mber Data Change" (PTO/SB/124).
Signature Paris becaus		
Typed or Printed Name Patrick D. Benedicto		
Date January 5, 2005		(408) 436-2110
NOTE: Signatures of all the inventors or assignees of record multiple forms if more than one signature is required, see belonger	of the entire interest or thow*.	neir representative(s) are required. Submit

This collection of information is required by 37 CFR 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



NATHE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
See attached table of Exhibit A	\
Serial No.:	{
Filing Date:	{
For:	}

POWER OF ATTORNEY AND REVOCATION OF PRIOR POWERS OF ATTORNEY

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Silicon Light Machines, owner of record of the applications and patents included within the table of Exhibit A attached hereto, hereby revokes all prior powers of attorney which may have been filed with these applications and patents and appoints Patrick D. Benedicto, Registration Number 40,909, of the firm of Okamoto & Benedicto, LLP, as its attorney with full power of substitution to transact all business in the U.S. Patent and Trademark Office in connection therewith.

Please direct all correspondence regarding these applications and patents to:

Patrick D. Benedicto, Esq. Okamoto & Benedicto, LLP 1737 North First Street, Suite 270 San Jose, California 95112 Phone: (408) 436-2110

Dated: 4/24/04

By: Charle & Rosco
Name: C. B. Rosco

Title: VP EN

EXHIBIT A

METHOD OF AND APPARATUS FOR SEALING A HERMETIC LID TO A SEMICONDUCTOR DIE
METHOD OF AND APPARATUS FOR SEALING A HERMETIC LID TO A SEMICONDUCTOR DIE
BANDWIDTH AND FRAME BUFFER SIZE REDUCTION IN A DIGITAL PULSE-WIDTH- MODULATED DISPLAY SYSTEM
TIME-INTERLEAVED BITPLANE, PULSE-WIDTH-MODULATION DIGITAL DISPLAY SYSTEM
DISPLAY/MONITOR WITH ORIENTATION DEPENDENT ROTATABLE IMAGE
METHOD AND APPARATUS FOR MODULATING A LIGHT BEAM
EMBOSSED DIFFRACTION GRATING ALONE AN IN COMBINATION WITH A CHANGEABLE IMAGE DISPLAY
DIFFRACTION GRATING LIGHT DOUBLING COLLECTION SYSTEM
A METHOD OF MAKING AND AN APPARATUS FOR A FLAT DIFFRACTION GRATING LIGHT VALVE
CLEAR-BEHIND MATRIX ADDRESSING FOR DISPLAY SYSTEMS
FLAT DIFFRACTION GRATING LIGHT VALVE

03900WO	03900	03800WO	03800	0, 3	03001	02901	02600	02502	02500	00, 00	01907	01900	OUR FILE#
PCT/US02/06 715	09/802,619	PCT/US03/15 213	10/187,159	08/961,826	08/821,394	08/821,390	09/092,245	09/467,083	09/092,220	09/104,159	09/500,118	08/920,122	NO.
2/28/02	3/8/01	5/14/03	6/28/02	10/31/97	3/20/97	3/20/97	6/5/98	12/20/99	6/25/98	6/24/95	2/8/00	9/2/97	FILING
Silicon Light Machines	Aki Tomita et al.	Silicon Light Machines	Jim Hunter et al.	Paul K. Manhart	David Bloom et al.	David Bloom et al.	Dave Corbin	Chris Gudeman	Chris Gudeman	David Bloom et al.	Dave Corbin et al.	Dave Corbin et al.	INVENTOR(S)/ APPLICANT
HIGH CONTRAST GRATING LIGHT VALVE	HIGH CONTRAST GRATING LIGHT VALVE	REDUCED FORMATION OF ASPERITIES IN CONTACT MICRO-STRUCTURES	REDUCED FORMATION OF ASPERITES IN CONTACT MICRO-STRUCTURES	DISPLAY APPARATUS INCLUDING GRATING LIGHT-VALVE ARRAY AND INTERFEROMETRIC OPTICAL SYSTEM	DISPLAY SYSTEM INCLUDING AN IMAGE GENERATOR AND MOVEABLE SCANNER FOR SAME	DISPLAY DEVICE INCORPORATING ONE DIMENSIONAL HIGH-SPEED GRATING LIGHT VALVE ARRAY	STEREO HEAD MOUNTED DISPLAY USING A SINGLE DISPLAY DEVICE	METHOD AND APPARATUS FOR APPLYING VAPOR PHASE LUBRICANT TO DEVICE	VAPOR PHASE LOW MOLECULAR WEIGHT LUBRICANTS	METHOD AND APPARATUS FOR MODULATING AN INCIDENT LIGHT BEAM FOR FORMING A TWO-DIMENSIONAL IMAGE	ELECTRICAL INTERFACE TO INTEGRATED CIRCUIT DEVICE HAVING HIGH DENSITY I/O COUNT	ELECTRICAL INTERFACE TO INTEGRATED CIRCUIT DEVICE HAVING HIGH DENSITY I/O COUNT	TITLE
				6,088,102	5,764,280	5,982,553	6,271,808	6,251,842 B1	6,004,912	6,215,579	6,452,260 B1	6,096,576	PATENT NO.
				7/11/00	6/9/98	11/9/99	8/7/01	6/26/01	12/21/99	4/10/01	9/17/02	8/1/00	ISSUED DATE

#ILING DATE 3 3/6/01 3 3/6/01 3 5/28/02 3 5/28/02 3 2/7/00 3 2/7/00 5 8/1/01 5 8/1/01 6 10/11/00 10/11/00 11/214/99 12/14/99 12/14/99 9/19/00 10 10 10 10 10 10 10 10 10 10 10 10 10 1			EIGHI				,
DATE APPLICANT APPLICANT APPLICANT APPLICANT APPLICANT APPLICANT METHOD AND APPARATUS FOR COMBINING LIGHT OUTPUT FROM MULTIPLE LASER DIODE BARS 5/28/02 Dave Corbin et al. OPTICAL DEVICE ARRAYS WITH OPTIMIZED IMAGE RESOLUTION OPTIMIZED IMAGE RESOLUTION OPTIMIZED IMAGE RESOLUTION SPECKLE CONTRAST REDUCTION BY POLARIZING AVERAGING SPECKLE CONTRAST REDUCTION BY POLARIZING AND PARATUS REBAM APPARATUS REBAM ATTENUATOR AND METHOD OF ATTENUATING A LASER BEAM 4/10/01 Rob Corrigan et al. METHOD, SYSTEM, AND DISPLAY APPARATUS FOR ELECTRONIC CINEMA		6,466,354 B1	METHOD AND APPARATUS FOR INTERFEROMETRIC MODULATION OF	Christopher Gudeman	9/19/00	09/664,020	05900
APPLICANT APPLICANT METHOD AND APPARATUS FOR COMBINING LIGHT OUTPUT FROM MULTIPLE LASER DIODE BARS S/28/02 Dave Corbin et al. OPTICAL DEVICE ARRAYS WITH OPTICAL DEVICE ARRAYS WITH Machines S/28/02 Dave Corbin et al. OPTICAL DEVICE ARRAYS WITH OPTICAL DEVICE ARRAYS WITH OPTICAL DEVICE ARRAYS WITH OPTICAL DEVICE ARRAYS WITH Machines SPECKLE CONTRAST REDUCTION BY POLARIZING AVERAGING AVERAGING GAS S/1/01 Chris Gudeman et ENCAPSULATED IMAGE RESOLUTION SPECKLE CONTRAST REDUCTION BY POLARIZING AVERAGING GAS SPECKLE CONTRAST REDUCTION BY POLARIZING AVERAGING GAS SPECKLE CONTRAST REDUCTION BY POLARIZING AVERAGING GAS SUBSPLAY APPARATUS RGB COLOR COMBINER AND 1D LIGHT VALVE RELAY INCLUDING SCHLIEREN FILTER SCHLIEREN FILTER SCHLIEREN FILTER INTEGRATED DRIVER PROCESS FLOW INTEGRATED DRIVER PRO	1		METHOD, SYSTEM, AND DISPLAY APPARATUS FOR ELECTRONIC CINEMA	Rob Corrigan et al.	4/10/01	09/832,738	05800
DATE INVENTORS)/ DATE APPLICANT APPLICANT METHOD AND APPARATUS FOR COMBINING LIGHT OUTPUT FROM MULTIPLE LASER DIODE BARS 5/28/02 Dave Corbin et al. OPTICAL DEVICE ARRAYS WITH OPTIMIZED IMAGE RESOLUTION //15 5/14/03 Silicon Light OPTIMIZED IMAGE RESOLUTION BY OPTIMIZED IMAGE RESOLUTION SPECKLE CONTRAST REDUCTION BY POLARIZING AVERAGING Chris Gudeman et al. SPECKLE CONTRAST REDUCTION BY POLARIZING AVERAGING S/5/02 Kenneth P. Gross AND LIGHT VALVE WITH ENCAPSULATED DAMPENING GAS DISPLAY APPARATUS RGB COLOR COMBINER AND 1D LIGHT VALVE RELAY INCLUDING SCHLIEREN FILTER DISPLAY APPARATUS RGB COLOR COMBINER AND 1D LIGHT VALVE RELAY INCLUDING SCHLIEREN FILTER S/28/02 Jim Hunter INTEGRATED DRIVER PROCESS FLOW Machines S/28/02 Jim Hunter INTEGRATED DRIVER PROCESS FLOW INTEGRATED DRIVER PROCESS FLOW INTEGRATED DRIVER PROCESS FLOW INTEGRATED DRIVER PROCESS FLOW INTEGRATED DAND APPARATUS FOR REDUCING COMBINER LASER SPECKLE		6,497,490 B1	LASER BEAM ATTENUATOR AND METHOD OF ATTENUATING A LASER BEAM	Greg Miller et al.	12/14/99	09/461,503	05700
DATE APPLICANT APPLICANT METHOD AND APPARATUS FOR COMBINING LIGHT OUTPUT FROM MULTIPLE LASER DIODE 5/28/02 Dave Corbin et al. OPTICAL DEVICE ARRAYS WITH OPTIMIZED IMAGE RESOLUTION 5/28/02 Dave Corbin et al. OPTICAL DEVICE ARRAYS WITH OPTIMIZED IMAGE RESOLUTION 5/14/03 Silicon Light Machines 9/5/02 Chris Gudeman et ENCAPSULATED DAMPENING GAS 9/5/02 Kenneth P. Gross AND 1D LIGHT VALVE WITH ENCAPSULATED DAMPENING GAS 9/5/02 Silicon Light Machines 5/28/02 Jim Hunter 11/21/02 Silicon Light AND 1D LIGHT VALVE RELAY INCLUDING SCHLIEREN FILTER 5/28/02 Jim Hunter INTEGRATED DRIVER PROCESS FLOW 1NTEGRATED DRIVER PROCESS FLOW INTEGRATED DRIVER PROCESS FLOW		6,323,984 B1	METHOD AND APPARATUS FOR REDUCING LASER SPECKLE	Jahja Trisnadi	10/11/00	09/687,465	05300
DATE INVENTOR(S)/ DATE APPLICANT METHOD AND APPARATUS FOR COMBINING LIGHT OUTPUT FROM MULTIPLE LASER DIODE 5/28/02 Dave Corbin et al. OPTICAL DEVICE ARRAYS WITH OPTIMIZED IMAGE RESOLUTION 5/14/03 Silicon Light OPTIMIZED IMAGE RESOLUTION 5/14/03 Silicon Light OPTIMIZED IMAGE RESOLUTION SPECKLE CONTRAST REDUCTION BY POLARIZING AVERAGING 8/1/01 Chris Gudeman et ENCAPSULATED DAMPENING GAS et al. ENCAPSULATED DAMPENING GAS 9/5/02 Kenneth P. Gross DISPLAY APPARATUS RGB COLOR COMBINER AND 1D LIGHT VALVE RELAY INCLUDING SCHLIEREN FILTER 11/21/02 Silicon Light AND 1D LIGHT VALVE RELAY INCLUDING SCHLIEREN FILTER 11/28/02 Jim Hunter INTEGRATED DRIVER PROCESS FLOW			INTEGRATED DRIVER PROCESS FLOW	Silicon Light Machines	5/14/03	PCT/US03/15 475	05200WO
DATE INVENTOR(S)/ DATE APPLICANT DATE APPLICANT METHOD AND APPARATUS FOR COMBINING LIGHT OUTPUT FROM MULTIPLE LASER DIODE 5/28/02 Dave Corbin et al. OPTICAL DEVICE ARRAYS WITH OPTIMIZED IMAGE RESOLUTION 5/14/03 Silicon Light OPTIMIZED IMAGE RESOLUTION SPECKLE CONTRAST REDUCTION BY POLARIZING AVERAGING 8/1/01 Chris Gudeman et ENCAPSULATED DAMPENING GAS 9/5/02 Kenneth P. Gross AND 1D LIGHT VALVE WITH AND 1D LIGHT VALVE RELAY INCLUDING SIlicon Light AND 1D LIGHT VALVE RELAY INCLUDING SCHLIEREN FILTER DISPLAY APPARATUS RGB COLOR COMBINER AND 1D LIGHT VALVE RELAY INCLUDING 6,692,129 B2 SCHLIEREN FILTER			INTEGRATED DRIVER PROCESS FLOW	Jim Hunter	5/28/02	10/161,191	05200
DATE APPLICANT METHOD AND APPARATUS FOR COMBINING LIGHT OUTPUT FROM MULTIPLE LASER DIODE 5/28/02 Dave Corbin et al. OPTICAL DEVICE ARRAYS WITH OPTIMIZED IMAGE RESOLUTION 1.5 5/14/03 Silicon Light OPTIMIZED IMAGE RESOLUTION Machines OPTIMIZED IMAGE RESOLUTION 2/7/00 Jahja Trisnadi SPECKLE CONTRAST REDUCTION BY POLARIZING AVERAGING 8/1/01 Chris Gudeman et ENCAPSULATED DAMPENING GAS 9/5/02 Kenneth P. Gross AND 1D LIGHT VALVE RELAY INCLUDING CH al. SIGHT VALVE RELAY INCLUDING 6,356,577 B1 RETHOD AND APPARATUS RGB COLOR COMBINER 6,646,778 B2 6,646,778 B2			JS RG	Silicon Light Machines	11/21/02	PCT/US02/37 573	05101WO
DATE INVENTOR(S)/ DATE APPLICANT METHOD AND APPARATUS FOR COMBINING LIGHT OUTPUT FROM MULTIPLE LASER DIODE 5/28/02 Dave Corbin et al. OPTICAL DEVICE ARRAYS WITH OPTICAL DEVICE ARRAYS WITH OPTIMIZED IMAGE RESOLUTION 1.5 5/14/03 Silicon Light Machines OPTIMIZED IMAGE RESOLUTION 2/7/00 Jahja Trisnadi POLARIZING AVERAGING 8/1/01 Chris Gudeman et ENCAPSULATED DAMPENING GAS 6,356,577 B1 6,356,577 B1 6,356,577 B1 6,366,778 B1	N)	6,692,129 B2	JS RGI Æ RE	Kenneth P. Gross et al.	9/5/02	10/237,239	05101
DATE INVENTOR(S)/ DATE APPLICANT METHOD AND APPARATUS FOR COMBINING LIGHT OUTPUT FROM MULTIPLE LASER DIODE 5/28/02 Dave Corbin et al. OPTICAL DEVICE ARRAYS WITH OPTIMIZED IMAGE RESOLUTION 2/7/00 Jahja Trisnadi POLARIZING AVERAGING PATENT NO. PATENT NO.		6,646,778 B2	GRATING LIGHT VALVE WITH ENCAPSULATED DAMPENING GAS	Chris Gudeman et al.	8/1/01	09/921,266	04400
DATE INVENTOR(S)/ DATE APPLICANT METHOD AND APPARATUS FOR COMBINING LIGHT OUTPUT FROM MULTIPLE LASER DIODE 5/28/02 Dave Corbin et al. OPTICAL DEVICE ARRAYS WITH OPTIMIZED IMAGE RESOLUTION 5/14/03 Silicon Light Machines OPTIMIZED IMAGE RESOLUTION OPTIMIZED IMAGE RESOLUTION			SPECKLE CONTRAST REDUCTION BY POLARIZING AVERAGING	Jahja Trisnadi	2/7/00	09/498,703	04300
DATE INVENTOR(S)/ DATE APPLICANT METHOD AND APPARATUS FOR COMBINING LIGHT OUTPUT FROM MULTIPLE LASER DIODE 5/28/02 Dave Corbin et al. OPTIMIZED IMAGE RESOLUTION PATENT NO. PATENT NO.			OPTICAL DEVICE ARRAYS WITH OPTIMIZED IMAGE RESOLUTION	Silicon Light Machines	5/14/03	PCT/US03/15 543	04100WO
DATE INVENTOR(S)/ DATE INVENTOR(S)/ TITLE PATENT NO. 3/6/01 Greg Miller LIGHT OUTPUT FROM MULTIPLE LASER DIODE 6,356,577 B1			OPTICAL DEVICE ARRAYS WITH OPTIMIZED IMAGE RESOLUTION	Dave Corbin et al.	5/28/02	10/161,193	04100
DATE APPLICANT PATENT NO.		6,356,577 B1	METHOD AND APPARATUS FOR COMBINING LIGHT OUTPUT FROM MULTIPLE LASER DIODE BARS	Greg Miller	3/6/01	09/354,643	04000
		PATENT NO.	TITLE	INVENTOR(S)/ APPLICANT	FILING	SERIAL NO.	OUR FILE#

07/00WO	07/00	0/601	076v0WO	07000WO	07000	06700WO	06700	06500	0υ+01	06400	06300	06100	OUR FILE#
602 PCT/US02/09	09/832,699	09/930,831	PCT/US02/09 604	PCT/US02/20 053	09/898,882	PCT/US03/15 348	10/187,137	09/930,820	10/321,342	09/930,838	09/574,733	10/047,5501	NO.
3/27/02	4/10/01	8/15/01	3/27/02	6/24/02	7/2/01	5/14/03	6/28/02	8/15/01	12/16/02	8/15/01	5/18/00	1/15/02	FILING
Silicon Light Machines	David Thomas Amm	David Thomas Amm	Silicon Light Machines	Silicon Light Machines	Jahja Trisnadi	Silicon Light Machines	David Amm	David Amm	David Amm et al.	David Amm et al.	Rob Corrigan	Gregory Miller	INVENTOR(S)/ APPLICANT
MODULATION FOR LIGHT OUT OF THE FOCAL PLANE, IN A GLV BASED PROJECTIONS SYSTEM	MODULATION FOR LIGHT OUT OF THE FOCAL PLANE, IN A GLV BASED PROJECTIONS SYSTEM	ANGLED ILLUMINATION FOR SINGLE ORDER, GLV BASED PROJECTION SYSTEM	ANGLED ILLUMINATION FOR SINGLE ORDER, GLV BASED PROJECTION SYSTEM	METHOD, APPARATUS AND DIFFUSER FOR REDUCING LASER SPECKLE (as amended in the ISR)	HADAMARD PHASE PATTERNS FOR OPTIMUM LASER SPECKLE REDUCTION	A METHOD AND DEVICE FOR MODULATING A LIGHT BEAM AND HAVING AN IMPROVED RESPONSE	A METHOD AND DEVICE FOR MODULATING A LIGHT BEAM AND HAVING AN IMPROVED RESPONSE	STRESS TUNED BLAZED GRATING LIGHT VALVE	BLAZED GRATING LIGHT MODULATOR	BLAZED GRATING LIGHT MODULATOR	PROJECTOR WITH SEPARATE LIGHT SOURCE AND PROJECTION OPTICS	METHOD FOR LITHOGRAPHY-BASED DOMAIN PATTERNING OF LOW-COERCIVE-FIELD FERRO ELECTRICS	TITLE
	6,614,580 B2	6,707,591 B2	·				6,714,337 B1	6,639,722 B2			6,480,634 B1		PATENT NO.
	9/2/03	3/16/04					3/30/04	10/28/03			11/12/02		ISSUED DATE

	08800	08700	08600	08500	06,70	08301	08300WO	08300	08200	08101	0/ 3	08000	07900	FILE #
	10/291,264	10/231,429	09/931,674	10/377,977	09/934,050	10/268,257	PCT/US02/27 822	09/952,626	09/875,499	10/100,627	09/765,922	10/051,972	09/782,387	NO.
	11/8/02	8/28/02	8/16/01	2/28/03	8/21/01	10/9/02	8/29/02	9/13/01	6/5/01	3/14/02	1/19/01	1/15/02	2/12/01	FILING
	Syed Tariq Shafaat	Syed Tariq Shafaat et al.	Omar Leung	Clinton Carlisle et al	Dinesh Maheshwari et al.	Michael Bruner	Silicon Light Machines	Michael Bruner	Gregory Miller et al.	Alexander Payne et al.	Alexander Payne et al.	Clinton Carlisle et al.	Clinton Carlisle et al.	INVENTOR(S)/ APPLICANT
COCKERN	METHOD OF MANUFACTURING OPTICAL	OPTICAL COUPLER	ENHANCE THERMAL STABILITY THROUGH OPTICAL SEGMENTATION	HIGH SENSITIVITY, HIGH RESOLUTION SPECTRAL CHANNEL MONITOR	METHOD AND APPARATUS FOR MEASURING WAVELENGTH JITTER OF LIGHT SIGNAL	MICROELECTRONIC MECHANICAL SYSTEM AND METHODS	MICROELECTRONIC MECHANICAL SYSTEM AND METHODS	MICROELECTRONIC MECHANICAL SYSTEM AND METHODS	FIBER OPTIC TRANSCEIVER	REDUCED SURFACE CHARGING IN SILICON-BASED DEVICES	REDUCED SURFACE CHARGING IN SILICON-BASED DEVICES	METHOD AND APPARATUS FOR DYNAMIC EQUALIZATION IN WAVELENGTH DIVISION MULTIPLEXING	AN ILLUMINATION SYSTEM FOR ONE- DIMENSIONAL SPATIAL LIGHT MODULATORS EMPLOYING MULTIPLE LIGHT SOURCES	TITLE
			6,587,253 B2					-		6,660,552 B2	6,387,723 B1		6,567,584 B2	PATENT NO.
			7/1/03							12/9/03	5/14/02		5/20/03	ISSUED DATE

	10600	10500	10400	10300	10200	10100	10000	09900	09700	09600	00500	09100	09000	08900	OUR FILE #
	10/327,357	10/318,658	10/159,715	10/225,370	10/179,664	10/112,962	10/256,558	10/367,440	10/323,560	10/225,211	10/183,585	10/160,305	10/231,346	10/232,790	SERIAL NO.
•	12/20/02	12/12/02	5/31/02	8/20/02	6/24/02	3/28/02	9/27/02	2/14/03	12/18/02	8/20/02	6/26/02	5/29/02	8/28/02	8/28/02	FILING
•	Clinton Carlisle et al.	Jahja Trisnadi et al.	Clinton Carlisle et al.	Jim Hunter et al.	Omar Leung	Mike Bruner et al.	Omar Leung et al.	Wihelmus de Groot et al.	Alexander Payne et al.	Alexander Payne et al.	Clinton Carlisle et al.	Josef Berger et al.	Syed Tariq Shafaat et al.	Syed Tariq Shafaat et al.	INVENTOR(S)/ APPLICANT
o	ARRANGEMENT FOR WDM MULTIPLEXING AND DEMULTIPLEXING	APPARATUS FOR SELECTIVELY BLOCKING WDM CHANNELS	LIGHT MODULATOR STRUCTURE FOR PRODUCING HIGH-CONTRAST OPERATION USING ZERO-ORDER LIGHT (as amended)	GRATING LIGHT VALVE WITH INDIVIDUALLY ADDRESSABLE RULINGS	METHOD TO CREATE A HERMETIC PACKAGE WITH TILTED COVER	MICROELECTRONIC MECHANICAL SYSTEM AND METHODS	CONTROLLED CURVATURE OF STRESSED MICRO-STRUCTURES	METHOD AND APPARATUS FOR LEVELING THERMAL STRESS IN MULTI-LAYER MEMS DEVICES	FABRY-PEROT INTERFEROMETER INCLUDING MEMBRANE SUPPORTED REFLECTOR	2-D DIFFRACTION GRATING FOR SUBSTANTIALLY ELIMINATING POLARIZATION DEPENDENT LOSSES	RAPIDLY TUNABLE EXTERNAL CAVITY LASER	OPTICAL SWITCH	OPTICAL COUPLER INCLUDING ALIGNMENT FEATURE	OPTICAL COMMUNICATION ARRANGEMENT	TITLE
							6,712,480 B1								PATENT NO.
							3/30/04			·				·	ISSUED DATE

		OF MAKENO THE SAME				
		MEM MICRO-STRUCTURES AND METHODS	James Hunter et al.	2/28/03	10/377,680	12100
		TILT-ABLE GRATING PLANE FOR IMPROVED CONTRAST IN 1XN STEPPED BLAZE SWITCHES	Dinesh Maheshwari	12/23/02	10/327,762	12000
		DIFFRACTIVE LIGHT MODULATOR WITH DYNAMICALLY ROTATABLE DIFFRACTION PLANE	Dinesh Maheshwari	2/28/03	10/377,936	11900
		PATTERNED GLV RIBBON FOR PDL REDUCTION	Michael Dueweke et al.	2/28/03	10/377,912	11800
	·	PRE-DEFLECTED BIAS RIBBONS	Chris Gudeman et al.	3/3/03	10/378,710	11700
		WAVELENGTH SIGNAL SWITCH AND EQUALIZER	Charles B. Roxlo	2/11/03	10/365,226	11600
		PDL MITIGATION STRUCTURE FOR DIFFRACTIVE MEMS AND GRATINGS	Dinesh Maheshwari et al.	2/28/03	10/377,840	11500
		MICRO-SUPPORT STRUCTURES	Silicon Light Machines	5/14/03	PCT/US03/15 217	11200WO
		MICRO-SUPPORT STRUCTURES	Jim Hunter et al.	6/28/02	10/186,911	11200
		A MEMS INTERFEROMETER-BASED CONFIGURABLE OPTICAL ADD-AND-DROP MULTIPLEXER	Jahja Trisnadi et al.	1/28/03	10/353,694	11100
		TWO-STAGE GAIN EQUALIZER	Robert Corrigan et al.	2/28/03	10/378,027	1100
		METHOD AND APPARATUS FOR DYNAMIC WAVELENGTH BAND SHAPING	Robert Corrigan	2/26/03	10/375,422	10900
		DIFFRACTIVE LIGHT MODULATOR-BASED DYNAMIC EQUALIZER WITH INTEGRATED SPECTRAL MONITOR (as amended)	Jahja Trisnadi et al.	5/30/02	10/159,330	10800
		CHIRPED OPTICAL MEM DEVICE	Dinesh Maheshwari et al.	12/18/02	10/323,008	10700
ISSUED DATE	PATENT NO.	TITLE	INVENTOR(S)/ APPLICANT	FILING DATE	SERIAL NO.	OUR FILE #

OUR	SERIAL	FILING	INVENTOR(S)/	TITLE	PATENT	ISSUED DATE
12200	10/377,994	2/28/03	James Hunter et al.	SILICON SUBSTRATE AS A LIGHT MODULATOR SACRIFICIAL LAYER		
12300	10/357,620	2/3/03	Clinton Carlisle et al.	A RECONFIGURABLE MODULATOR-BASED OPTICAL ADD-AND-DROP MULTIPLEXER		
12400	10/322,202	12/17/02	Dinesh Maheshwari	HIGH CONTRAST TILTING RIBBON BLAZED GRATING	·	
1. 0	10/322,213	12/17/02	Dinesh Maheshwari	ARBITRARY PHASE PROFILE FOR BETTER EQUALIZATION IN DYNAMIC GAIN EQUALIZER		
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